

Form PTO-1449 (modified)Atty. Docket No.
SILA:019Serial No.
09/034,453

List of Patents and Publications for Applicant's

Applicant
Jerrell P. Hein et al.**INFORMATION DISCLOSURE STATEMENT**

(Use several sheets if necessary)

Filing Date:
March 4, 1998

Group:

U.S. Patent Documents
See Pages 1,2,3 and 4Foreign Patent Documents
N/AOther Art
See Pages 1,2 and 4**U.S. Patent Documents**

| Exam. Init. | Ref. Des. | Document Number | Date | Name | Class | Sub Class | Filing Date if App. |
|-------------|-----------|-----------------|---------------|----------------|-------|-----------|---------------------|
| Rs | A1 | 4,292,595 | Sep. 29, 1981 | Smith | 330 | 10 | Nov. 13, 1979 |
| | A2 | 4,835,486 | May 30, 1989 | Somerville | 330 | 10 | May 10, 1988 |
| | A3 | 4,843,339 | Jun. 27, 1989 | Burt et al. | 330 | 10 | Oct. 28, 1987 |
| | A4 | 4,901,275 | Feb. 13, 1990 | Hardie et al. | 364 | 900 | Sep. 4, 1987 |
| Rs | A5 | 5,287,107 | Feb. 15, 1994 | Gampell et al. | 341 | 137 | Jun. 5, 1992 |

Foreign Patent Documents

| Exam. Init. | Ref. Des. | Document Number | Date | Name | Class | Sub Class | Translation Yes/No |
|-------------|-----------|-----------------|------|------|-------|-----------|--------------------|
| | B1 | N/A | | | | | |
| | B2 | | | | | | |

Other Art (Including Author, Title, Date, Pertinent Pages, Etc.)

| Exam. Init. | Ref. Des. | Citation |
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| Rs | C1 | Boser et al., <i>The Design of Sigma-Delta Modulation Analog-to-Digital Converters</i> , IEEE J. Solid-State Circuits, vol. SC-23, pp. 1298-1308, December 1988. |
| | C2 | Candy, <i>A Use of Double Integration in Sigma Delta Modulation</i> , IEEE Trans. Commun., vol. COM-33, pp. 249-258, March 1985. |
| Rs | C3 | Naus, et al., <i>A CMOS Stereo 16-bit D/A Converter for Digital Audio</i> , IEEE J. Solid-State Circuits, vol. SC-22, pp. 390-395, June 1987. |

Examiner:

Ram Sh

Date Considered:

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EXAMINER: initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

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| Rs | A6 | 5,369,666 | Nov. 29, 1994 | Folwell et al. | 375 | 8 | Jun. 9, 1992 |
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Foreign Patent Documents

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| Exam. Init. | Ref. Des. | Citation |
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| RS | C4 | Silicon Systems, <i>SSI 73D2950/2950T Low Power FAX/Data Modem Chip Set Advance Information</i> , pp. 4-75 - 4-105, December 1993. |
| J | C5 | Siemens, " <i>Ics For Communications</i> " <i>Analog Line Interface Solution ALIS</i> ", PSB 4595 Version 2.0 and PSB 4596 Version 2.1 Product Overview 08.97, 19 pages (1997) |
| J | C6 | Krypton, " <i>K²930G Functional and Electrical Descriptions</i> ", 10 pages (1997) |
| RS | C7 | Lucent Technologies, <i>LUCL8576 Dual Subscriber Line Interface Circuit</i> , Advanced Data Sheet Issue 1.3, 16 pages, January 8, 1997. |

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